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<u>Silicon Carbide (SiC)</u> <u>MOSFET</u> - EliteSiC, 32 mohm, 650 V, M3S, TO-247-4L

NVH4L032N065M3S

Features

- Typical $R_{DS(on)} = 32 \text{ m}\Omega @ V_{GS} = 18 \text{ V}$
- Ultra Low Gate Charge ($Q_{G(tot)} = 55 \text{ nC}$)
- High Speed Switching with Low Capacitance ($C_{oss} = 114 \text{ pF}$)
- 100% Avalanche Tested
- AEC-Q101 Qualified and PPAP Capable
- This Device is Halide Free and RoHS Compliant with Exemption 7a, Pb–Free 2LI (on second level interconnection)

Applications

- Automotive On Board Charger
- Automotive DC-DC Converter for EV/HEV

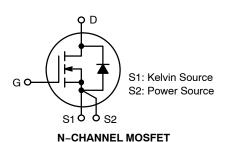
MAXIMUM RATINGS (T_J = 25° C unless otherwise noted)

Parameter	Symbol	Value	Unit	
Drain-to-Source Voltage	V _{DSS}	650	V	
Gate-to-Source Voltage	Gate-to-Source Voltage		-8/+22	V
Continuous Drain Current	$T_C = 25^{\circ}C$	I _D	50	А
Power Dissipation	T _C = 25°C	PD	187	W
Continuous Drain Current (Note 1)	T _C = 100°C	Ι _D	30	A
Power Dissipation	T _C = 100°C	PD	94	W
Pulsed Drain Current (Note 2)	T _C = 25°C t _p = 100 μs	I _{DM}	163	A
Continuous Source-Drain Current (Body Diode)	$T_{C} = 25^{\circ}C$ $V_{GS} = -3 V$	I _S	29	A
Continuous Source-Drain Current (Body Diode)	T _C = 100°C V _{GS} = -3 V	I _S	16	A
Pulsed Source-Drain Current (Body Diode) (Note 2)	$T_{C} = 100^{\circ}C$ $V_{GS} = -3 V$ $t_{p} = 100 \ \mu s$	I _{SM}	137	A
Single Pulse Avalanche Energy (Note 3)	I _{LPK} = 16.7 A, L = 1 mH	E _{AS}	139	mJ
Operating Junction and Storage T Range	T _J , T _{stg}	–55 to +175	°C	
Lead Temperature for Soldering P (1/8" from case for 10 seconds)	ΤL	270	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

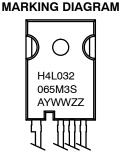
- 1. 30 A is limited by package. Power chip max drain current is 35 A if limited by max junction temperature.
- Repetitive rating, limited by max junction temperature.
- 3. EAS of 139 mJ is based on starting $T_J = 25^{\circ}$ C, L = 1 mH, $I_{AS} = 16.7$ A, $V_{DD} = 100$ V, $V_{GS} = 18$ V.

V _{(BR)DSS}	R _{DS(ON)} TYP	I _D MAX	
650 V	32 m Ω @ V _{GS} = 18 V	50 A	





TO-247-4L CASE 340CJ



H4L032065M3S = Specific Device Code

A = Assembly Location

- WW = Work Week
- ZZ = Lot Traceability

ORDERING INFORMATION

Device	Package	Shipping
NVH4L032N065M3S	TO-247-4L	30 Units / Tube

Y = Year

THERMAL CHARACTERISTICS

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Note 4)	$R_{\theta JC}$	0.8	°C/W
Thermal Resistance, Junction-to-Ambient (Note 4)	R_{\thetaJA}	40	

4. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

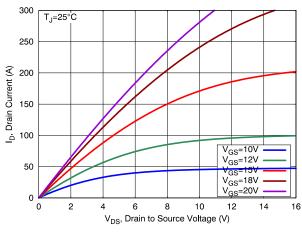
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
OFF CHARACTERISTICS	•		•			
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0 V, I _D = 1 mA, T _J = 25°C	650	-	-	V
Drain-to-Source Breakdown Voltage Temperature Coefficient	$\Delta V_{(BR)DSS}/ \Delta T_J$	I_D = 1 mA, Referenced to 25°C	-	90	-	mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} = 650 V, T_J = 25°C	-	-	10	μA
		V _{DS} = 650 V, T _J = 175°C (Note 6)	-	-	500	μΑ
Gate-to-Source Leakage Current	I _{GSS}	$V_{GS} = -8/+22$ V, $V_{DS} = 0$ V	-	-	±1.0	μΑ
ON CHARACTERISTICS	-		-	-	-	-
Drain-to-Source On Resistance	R _{DS(on)}	V_{GS} = 18 V, I_D = 15 A, T_J = 25°C	-	32	44	mΩ
		V _{GS} = 18 V, I _D = 15 A, T _J = 175°C (Note 6)	-	49	_	
		V_{GS} = 15 V, I_{D} = 15 A, T_{J} = 25°C	-	41	-	
		V _{GS} = 15 V, I _D = 15 A, T _J = 175°C (Note 6)	-	52	_	
Gate Threshold Voltage	V _{GS(TH)}	V_{GS} = V_{DS} , I_D = 7.5 mA, T_J = 25°C	2	2.9	4	V
Forward Transconductance	9fs	V _{DS} = 10 V, I _D = 15 A (Note 6)	-	9.9	_	S
CHARGES, CAPACITANCES & GATE	RESISTANCE					
Input Capacitance	C _{ISS}	V _{DS} = 400 V, V _{GS} = 0 V, f = 1 MHz (Note 6)	-	1410	-	pF
Output Capacitance	C _{OSS}		-	114	-	
Reverse Transfer Capacitance	C _{RSS}		-	9.2	-	
Total Gate Charge	Q _{G(TOT)}	$V_{DD} = 400 \text{ V}, \text{ I}_{D} = 15 \text{ A},$	-	55	-	nC
Gate-to-Source Charge	Q _{GS}	V _{GS} = -3/18 V (Note 6)	-	15	-	1
Gate-to-Drain Charge	Q _{GD}		-	14	-	
Gate Resistance	R _G	f = 1 MHz	-	5.0	-	Ω
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	t _{d(ON)}	V_{GS} = -3/18 V, V_{DD} = 400 V, I _D = 15 A, R _G = 4.7 Ω, T _J = 25°C (Notes 5 and 6)	-	8.8	-	ns
Turn-Off Delay Time	t _{d(OFF)}		-	31	-]
Rise Time	t _r		-	12	-	
Fall Time	t _f		-	9	-	
Turn-On Switching Loss	E _{ON}		-	33	-	μJ
Turn-Off Switching Loss	E _{OFF}		-	16	-	
Total Switching Loss	E _{TOT}		-	49	-	1

ELECTRICAL CHARACTERISTICS (T_{.1} = 25°C unless otherwise specified) (continued)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
SWITCHING CHARACTERISTICS	-					
Turn-On Delay Time	t _{d(ON)}	$V_{GS} = -3/18 \text{ V}, V_{DD} = 400 \text{ V},$	-	7.8	-	ns
Turn-Off Delay Time	t _{d(OFF)}	I _D = 15 A, R _G = 4.7 Ω, T _J = 175°C (Notes 5 and 6)	-	37	-	
Rise Time	t _r		-	12	-	
Fall Time	t _f		-	11	-	
Turn-On Switching Loss	E _{ON}		-	31	-	μJ
Turn–Off Switching Loss	E _{OFF}		-	25	-	
Total Switching Loss	E _{TOT}	1	-	56	-	
SOURCE-TO-DRAIN DIODE CHARA	CTERISTICS					
Forward Diode Voltage	V _{SD}	I_{SD} = 15 A, V_{GS} = –3 V, T_J = 25°C	-	4.5	6.0	V
		I _{SD} = 15 A, V _{GS} = -3 V, T _J = 175°C (Note 6)	-	4.1	_	
Reverse Recovery Time	t _{RR}	$\label{eq:VGS} \begin{array}{l} V_{GS} = -3 \ V, \ I_S = 15 \ A, \\ dI/dt = 1000 \ A/\mu s, \ V_{DS} = 400 \ V, \\ T_J = 25^\circ C \ (Note \ 6) \end{array}$	-	15.5	-	ns
Charge Time	t _a		-	8.9	-	
Discharge Time	t _b		-	6.6	-	1
Reverse Recovery Charge	Q _{RR}		-	72	-	nC
Reverse Recovery Energy	E _{REC}		-	4.6	-	μJ
Peak Reverse Recovery Current	I _{RRM}		-	9.3	-	А

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.
5. EON/EOFF result is with body diode.
6. Defined by design, not subject to production test.

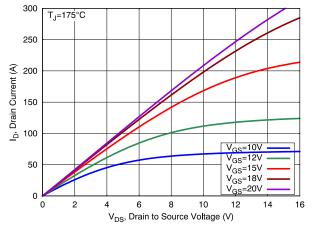
TYPICAL CHARACTERISTICS





250

V_{DS}=10V





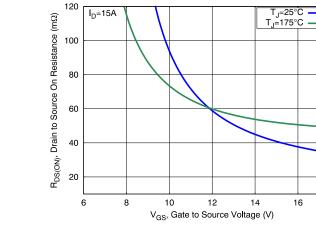


Figure 4. On-Resistance vs. Gate Voltage

18

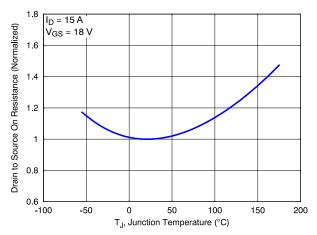
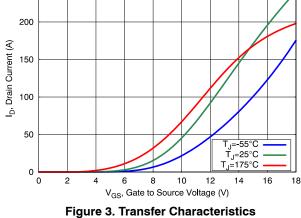


Figure 6. On-Resistance vs. Junction Temperature



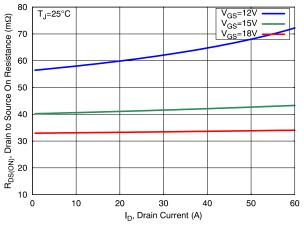
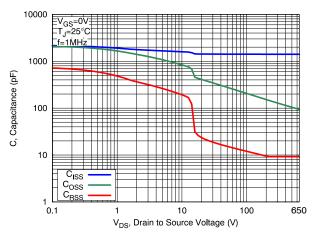


Figure 5. On-Resistance vs. Drain Current

TYPICAL CHARACTERISTICS





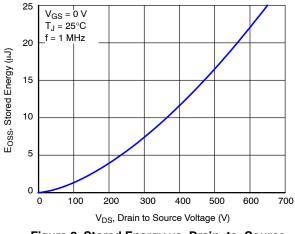
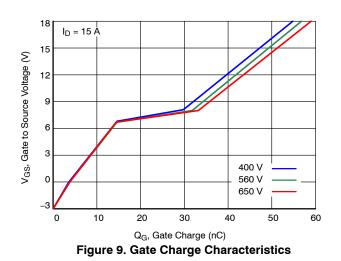
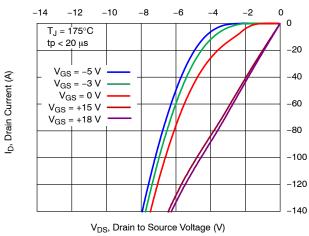


Figure 8. Stored Energy vs. Drain-to-Source Voltage







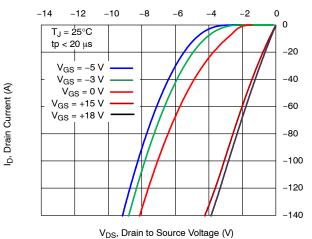
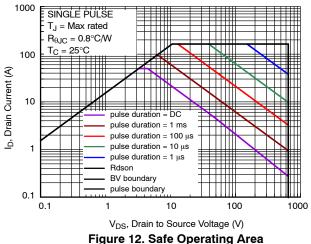


Figure 10. Reverse Conduction Characteristics



TYPICAL CHARACTERISTICS

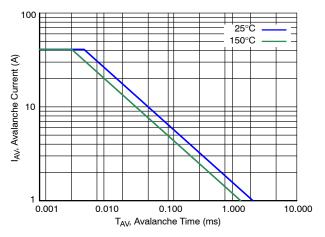


Figure 13. Avalanche Current vs. Pulse Time (UIS)

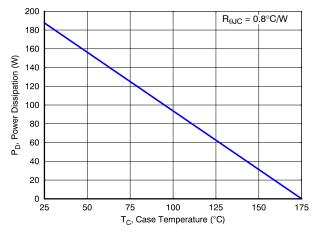


Figure 14. Maximum Power Dissipation vs. Case Temperature

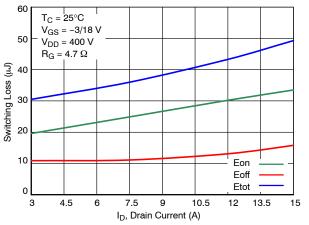


Figure 15. Inductive Switching Loss vs. Drain Current

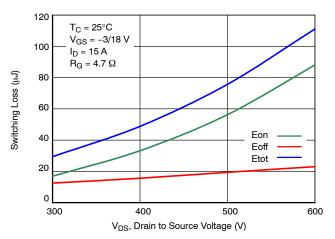


Figure 17. Inductive Switching Loss vs. Drain Voltage

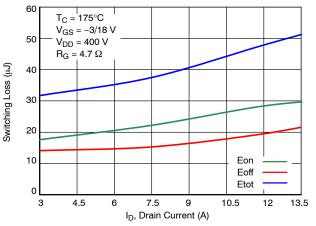


Figure 16. Inductive Switching Loss vs. Drain Current

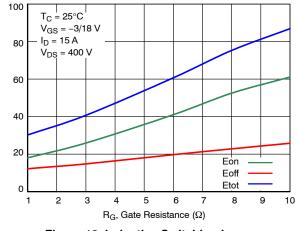
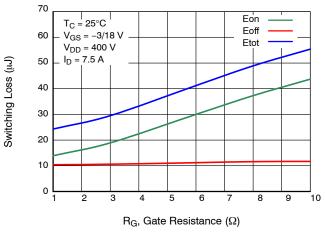


Figure 18. Inductive Switching Loss vs. Gate Resistance

Switching Loss (µJ)

TYPICAL CHARACTERISTICS





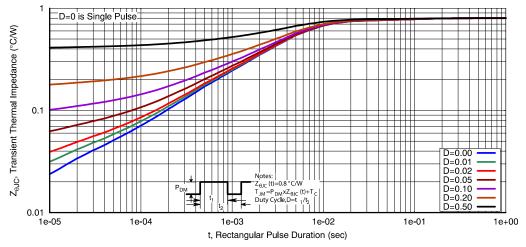


Figure 20. Thermal Response Characteristics



TO-247-4LD CASE 340CJ **ISSUE A**

DATE 16 SEP 2019

NOM

5.00

2.40

2.00

1.20

1.40

2.22

0.60

22.54

16.25

1.17

2.54 BSC

5.08 BSC

15.60

13.00

5.00

18.42

2.62

3.60

6.80

6.17

6.17

3.40

6.60

5.97

5.97

р p1

Q

S

MAX

5.20

2.70

2.20

1.33

1.60

2.42

0.70

22.74

16.50

1.37

15.80

13.20

5.20

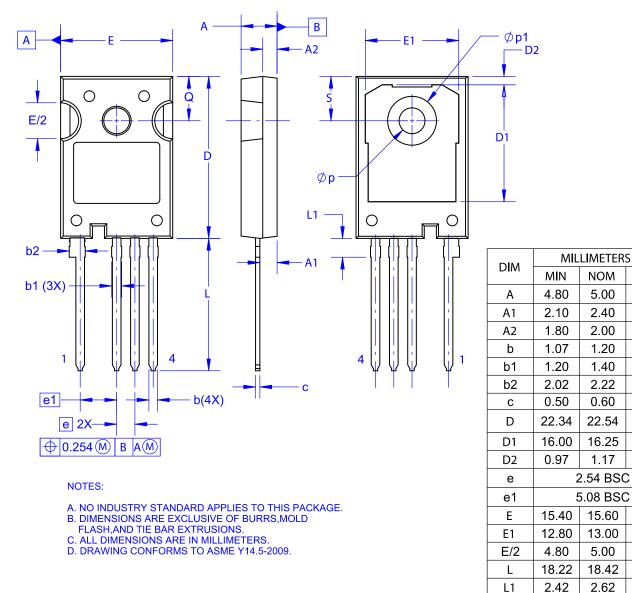
18.62

2.82

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6.37



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