

SiC JFET Division

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Silicon Carbide (SiC) Diode - EliteSiC, TO-220-2L, 10 A, 650 V SiC Merged PiN-Schottky (MPS) Diode | UJ3D06510TS

Datasheet

Description

United Silicon Carbide, Inc. offers the 3rd generation of high performance SiC Merged-PiN-Schottky (MPS) diodes. With zero reverse recovery charge and 175°C maximum junction temperature, these diodes are ideally suited for high frequency and high efficiency power systems with minimum cooling requirements.

CASE CASE RoHS AEC-Q101 Halogen Free Green

Part Number	Package	Marking		
UJ3D06510TS	TO-220-2L	UJ3D06510TS		

Features

- ◆ 175°C maximum operating junction temperature
- Easy paralleling
- Extremely fast switching not dependent on temperature
- No reverse or forward recovery
- Enhanced surge current capability, MPS structure
- Excellent thermal performance, Ag sintered
- 100% UIS tested
- ◆ AEC-Q101 qualified
- AECQ Qualified

Typical Applications

- Power converters
- Industrial motor drives
- Switching-mode power supplies
- Power factor correction modules

Maximum Ratings

Parameter	Symbol	Test Conditions	Value	Units	
DC blocking voltage	V _R		650	V	
Repetitive peak reverse voltage, T _j =25°C	V_{RRM}		650	V	
Surge peak reverse voltage	V _{RSM}		650	V	
Maximum DC forward current	I _F	T _C = 152°C	10	Α	
Non-repetitive forward surge current		$T_C = 25$ °C, $t_p = 10$ ms	70	A	
sine halfwave	I _{FSM}	T _C = 110°C, t _p =10ms	60		
Repetitive forward surge current		$T_C = 25$ °C, $t_p = 10$ ms	45.9		
sine halfwave, D=0.1	I _{FRM}	$T_C = 110^{\circ}C$, $t_p = 10$ ms	28.7	A	
		$T_{C} = 25^{\circ}C, t_{p} = 10 \mu s$	455	А	
Non-repetitive peak forward current	I _{F,max}	$T_{C} = 110^{\circ}\text{C}, t_{p} = 10 \mu\text{s}$	455		
i ² t value	∫ i²dt	$T_C = 25$ °C, $t_p = 10$ ms	24.5	A ² s	
i t value		$T_C = 110$ °C, $t_p = 10$ ms	18		
D		T _C = 25°C	136.4	W	
Power dissipation	P _{Tot}	T _C = 152°C	20.9		
Maximum junction temperature	T _{J,max}		175	°C	
Operating and storage temperature	T _J , T _{STG}		-55 to 175	°C	
Soldering temperatures, wavesoldering only allowed at leads	T _{sold}	1.6mm from case for 10s	260	°C	

Datasheet

Electrical Characteristics

 $T_1 = +25$ °C unless otherwise specified

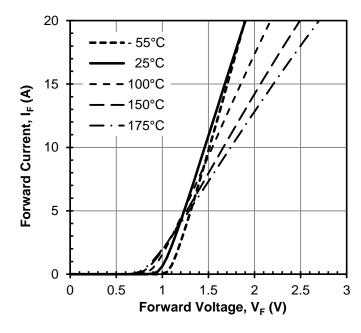
Parameter	Symbol	Test Conditions	Value			11-24-
			Min	Тур	Max	Units
	V _F	I _F = 10A, T _J = 25°C	-	1.5	1.7	V
Forward voltage		I _F = 10A, T _J =150°C	-	1.68	2.0	
		I _F = 10A, T _J =175°C	-	1.75	2.1	
Reverse current	I _R	V _R =650V, T _j =25°C	-	10	60	μА
neverse current		V _R =650V, T _J =175°C	-	150		
Total capacitive charge ⁽¹⁾	Q_{C}	V _R =400V		23		nC
	С	V _R =1V, f=1MHz		327		pF
Total capacitance		V _R =300V, f=1MHz		38		
		V _R =600V, f=1MHz		34		
Capacitance stored energy	E _C	V _R =400V		3.4		μЈ

⁽¹⁾ Q_c is independent on T_i , di_F/dt , and I_F as shown in the application note USCi_AN0011.

Thermal characteristics

Parameter	symbol	Test Conditions	Value			Units
			Min	Тур	Max	Offics
Thermal resistance, junction - case	$R_{\theta JC}$			0.82	1.1	°C/W

Typical Performance





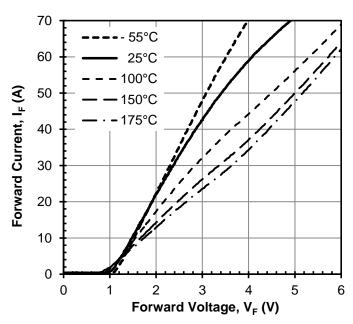
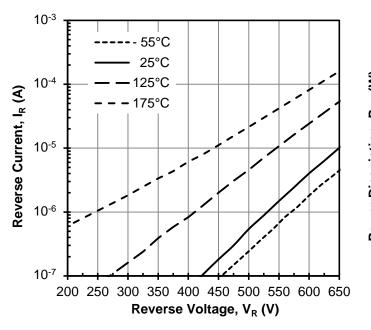


Figure 2 Typical forward characteristics in surge current

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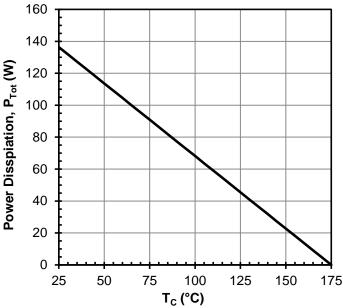
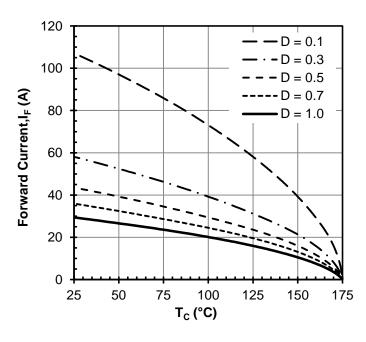


Figure 3 Typical reverse characteristics

Figure 4 Power dissipation



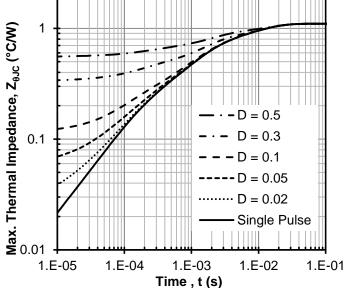


Figure 5 Diode forward current

Figure 6 Maximum transient thermal impedance

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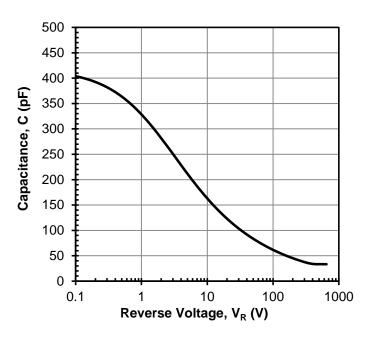


Figure 7 Capacitance vs. reverse voltage at 1MHz

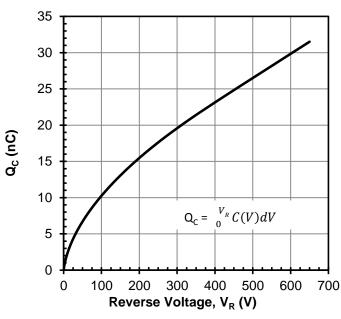


Figure 8 Typical capacitive charge vs. reverse voltage

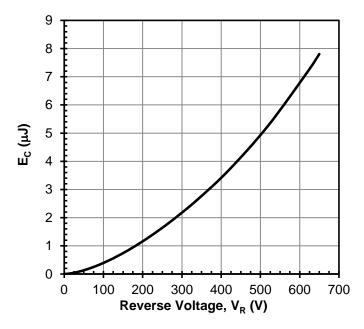


Figure 9 Typical capacitance stored energy vs. reverse voltage

Silicon Carbide (SiC) Diode - EliteSiC, TO-220-2L, 10A, 650V SiC Merged PiN-Schottky (MPS) Diode | UJ3D06510TS

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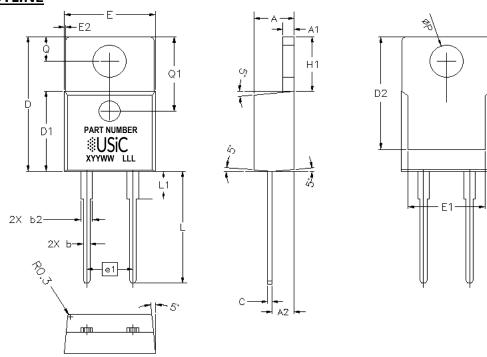
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TO-220-2L PACKAGE OUTLINE, PART MARKING AND TUBE SPECIFICATIONS

PACKAGE OUTLINE

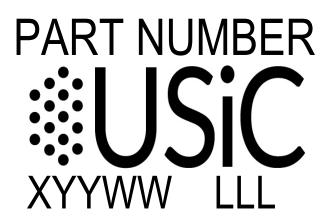


DIM	INC	HES	MILLIN	METERS
	MIN	MAX	MIN	MAX
Α	0.140	0.190	3.56	4.83
A1	0.020	0.055	0.51	1.40
A2	0.080	0.115	2.03	2.92
b	0.015	0.040	0.38	1.02
b2	0.040	0.070	1.02	1.78
С	0.014	0.030	0.36	0.76
D	0.560	0.650	14.22	16.51
D1	0.330	0.370	8.38	9.40
D2	0.480	0.517	12.19	13.13
Е	0.380	0.420	9.65	10.67
e1	0.200	O BSC	5.08	BSC
E1	0.270	0.350	6.86	8.89
E2	-	0.030		0.76
L	0.495	0.580	12.57	14.73
L1	-	0.250	1	6.35
ØΡ	0.139	0.161	3.53	4.09
Н	0.230	0.270	5.84	6.86
Q	0.100	0.135	2.54	3.43
Q1	0.330	0.340	8.38	8.64



TO-220-2L PACKAGE OUTLINE, PART MARKING AND TUBE SPECIFICATIONS

PART MARKING



PART NUMBER = REFER TO
DS PN DECODER FOR DETAILS

X = ASSEMBLY SITE

YY = YEAR

WW = WORK WEEK

LLL = LOT ID

PACKING TYPE

ANTI-STATIC TUBE

QUANTITY /TUBE: 50 UNITS

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