

SiC JFET Division

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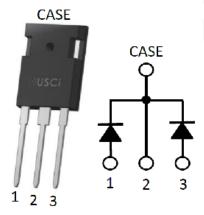






DATASHEET

UJ3D06560KSD



Part Number	Package	Marking
UJ3D06560KSD	TO-247-3L	UJ3D06560KSD









Silicon Carbide (SiC) Diode - EliteSiC, TO-247-3L, 60 A, 650 V SiC Merged PiN-Schottky (MPS) Diode

Rev. D, Jan 2025

Description

United Silicon Carbide, Inc. offers the 3rd generation of high performance SiC Merged-PiN-Schottky (MPS) diodes. With zero reverse recovery charge and 175°C maximum junction temperature, these diodes are ideally suited for high frequency and high efficiency power systems with minimum cooling requirements.

Features

- 175°C maximum operating junction temperature
- Easy paralleling
- Extremely fast switching not dependent on temperature
- No reverse or forward recovery
- Enhanced surge current capability, MPS structure
- Excellent thermal performance, Ag sintered
- 100% UIS tested
- AEC-Q101 qualified
- AECQ Qualified

Typical applications

- Power converters
- Industrial motor drives
- Switching-mode power supplies
- Power factor correction modules

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Maximum Ratings

Paramater	Symbol	Test Conditions	Value (Leg/ Device)	Units	
DC blocking voltage	V _R		650	V	
Repetitive peak reverse voltage, T _i =25°C	V _{RRM}		650	V	
Surge peak reverse voltage	V _{RSM}		650	V	
Maximum DC forward current	I _F	T _C = 140°C	30/60	Α	
Non-repetitive forward surge current sine halfwave	I _{FSM}	$T_{\rm C} = 25^{\rm o}$ C, $t_{\rm p} = 10$ ms	165/330	Α	
		$T_{c} = 110^{\circ}C, t_{p} = 10 \text{ms}$	150/300		
Repetitive forward surge current sine halfwave, D=0.1	I _{FRM}	$T_{c} = 25^{\circ}C, t_{p} = 10 \text{ms}$	107.2/214.4	A	
		$T_{c} = 110^{\circ}C, t_{p} = 10 \text{ms}$	66.1/132.2		
Non-repetitive peak forward current	I _{F, max}	$T_{c} = 25^{\circ}C, t_{p} = 10\mu s$	1250/2500	Α	
		$T_{c} = 110^{\circ}C, t_{p} = 10\mu s$	1250/2500		
i²t value	∫i²dt	$T_{c} = 25^{\circ}C, t_{p} = 10 \text{ms}$	136/544	A ² s	
		$T_{c} = 110^{\circ}C, t_{p} = 10 \text{ms}$	112/448		
Diode dV/dt ruggedness	dV/dt	V _R = 0 - 650V	200	V/ns	
Power dissipation	P _{tot}	T _C = 25°C	288.5/577	W	
		T _C = 140°C	67.3/134.6		
Maximum junction temperature	T _{J, max}		175	°C	
Operating and storage temperature	T _J , T _{STG}		-55 to 175	°C	
Soldering temperatures, wavesoldering only allowed at	T _{sold}	1.6mm from case for 10s	260	°C	
leads					

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Electrical Characterists

			Value (Leg/Device)				
Parameter	Symbol	Test Conditions	Min	Тур	Max	Units	
Forward voltage	V _F	I _F = 30A/60A, T _J = 25°C	-	1.5	1.7	V	
		I _F = 30A/60A, T _J =	-	1.77	2.10		
		150°C					
		I _F = 30A/60A, T _J =	-	1.85	2.25		
		175°C					
Reverse current	I _R	V _R = 650V, T _J = 25°C	-	30/60	370/740	μΑ	
		V _R = 650V, T _J = 175°C	-	390/780	-		
Total capacitive charge (3)	Q _c	V _R = 400V	-	72/144	-	nC	
Total capacitance	С	V _R = 1V, f = 1MHz	-	990/	-	PF	
				1980			
		V _R = 300V, f = 1MHz	-	117/234	-		
		V _R = 600V, f = 1MHz	-	101/202	-		
Capacitance stored energy	E _c	V _R = 400V	-	10.5/21	-	μJ	

(1) QC is independent on TJ, $di_{\rm F}/dt$, and IF as shown in the application note USCi_AN0011

Thermal characteristics			Value (Leg/Device)			
Parameter	Symbol	Test Conditions	Min	Тур	Max	Units
Thermal resistance, junction -	$R_{\theta JC}$		-	0.4/0.2	0.52/0.26	°C/W
case						

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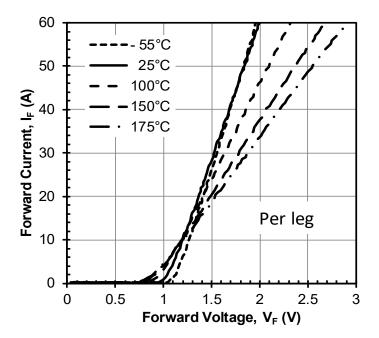


Figure 1. Typical forward characteristics

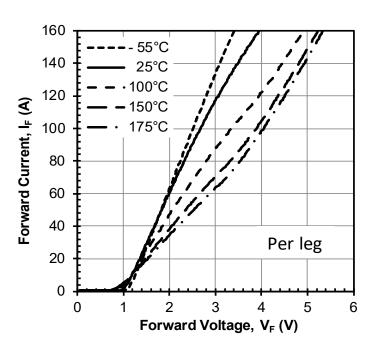


Figure 2. Typical forward characteristics in surge current

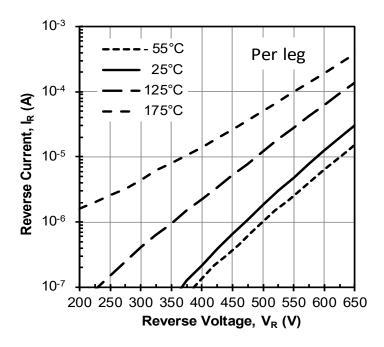


Figure 3. Typical reverse characteristics

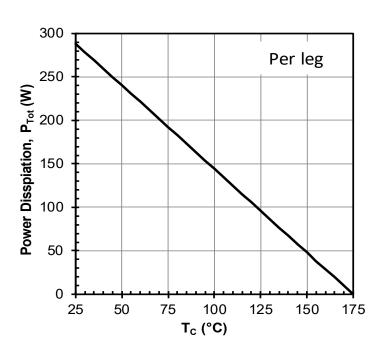


Figure 4. Power dissipation

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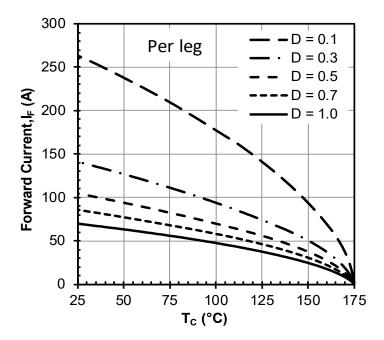


Figure 5. Diode forward current

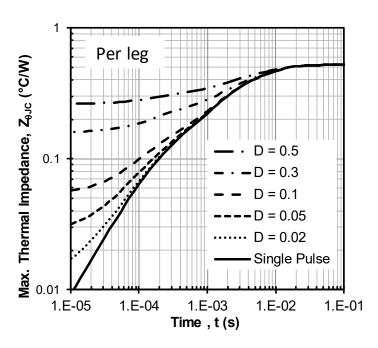


Figure 6. Maximum transient thermal impedance

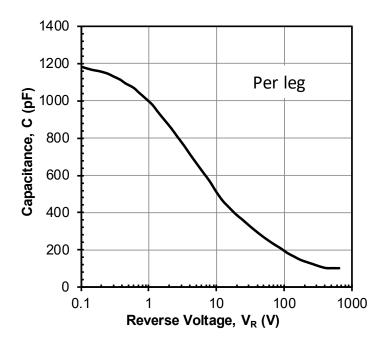


Figure 7. Capacitance vs. reverse voltage at 1MHz

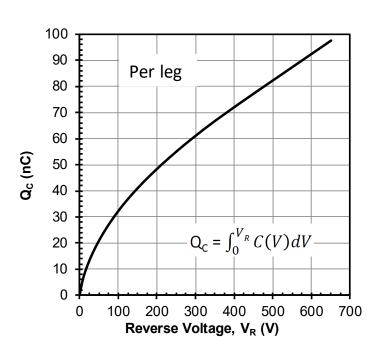


Figure 8. Typical capacitive charge vs. reverse voltage

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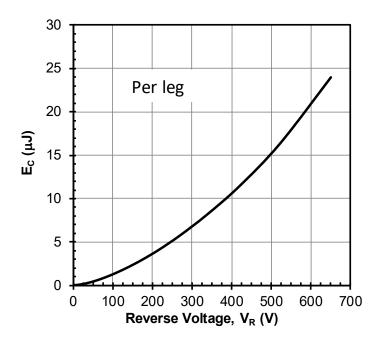


Figure 9. Typical capacitance stored energy vs. reverse voltage

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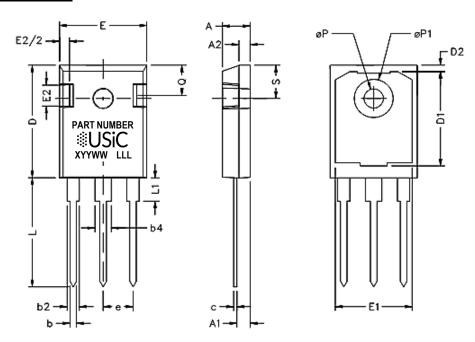
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TO-247-3L PACKAGE OUTLINE, PART MARKING AND TUBE SPECIFICATIONS

PACKAGE OUTLINE

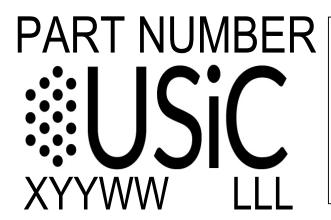


SYM	INCHES		MILLIMETERS		
	MIN	MAX	MIN	MAX	
Α	0.185	0.209	4.699	5.309	
A1	0.087	0.102	2.21	2.61	
A2	0.059	0.098	1.499	2.489	
b	0.039	0.055	0.991	1.397	
b2	0.065	0.094	1.651	2.388	
b4	0.102	0.135	2.591	3.429	
С	0.015	0.035	0.381	0.889	
D	0.819	0.845	20.803	21.463	
D1	0.515	-	13.081	-	
D2	0.02	0.053	0.508	1.346	
E	0.61	0.64	15.494	16.256	
е	0.214	4 BSC	5.44 BSC		
E1	0.53	-	13.462	-	
E2	0.135	0.157	3.429	3.988	
L	0.78	0.8	19.812	20.32	
L1	ı	0.177	ī	4.496	
ØΡ	0.14	0.144	3.556	3.658	
ØP1	0.278	0.291	7.061	7.391	
Q	0.212	0.244	5.385	6.198	
S	0.243	3 BSC	6.17	BSC	



TO-247-3L PACKAGE OUTLINE, PART MARKING AND TUBE SPECIFICATIONS

PART MARKING



PART NUMBER = REFER TO
DS PN DECODER FOR DETAILS

X = ASSEMBLY SITE

YY = YEAR

WW = WORK WEEK

LLL = LOT ID

PACKING TYPE

ANTI-STATIC TUBE

QUANTITY /TUBE: 30 UNITS

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